

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	C.M. LE et al.	Examiner	Prenell P. Jones
Serial No.	10/783,435	Group Art Unit	2616
Filed	February 20, 2004	Docket No.	SJO920030043US1
TITLE	METHOD, SYSTEM, AND PROGRAM FOR CHECKING AND REPAIRING A NETWORK CONFIGURATION		

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CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being transmitted through the USPTO EFS-Web system over the Internet to Prenell P. Jones of the U.S. Patent and Trademark Office on January 29, 2008.

/Janaki K. Davda/

Janaki K. Davda Reg. No. 40,684

STATEMENT OF SUBSTANCE OF INTERVIEW

Dear Examiner Prenell:

Telephone interviews were held on Tuesday, January 29, 2008 between Examiner Prenell and Applicants' representative, Janaki K. Davda. During the telephone interviews, Ms. Davda authorized Examiner Prenell to remove the language "firmware, programmable logic" from the specification, page 16, as follows:

The term "article of manufacture" as used herein refers to code or logic implemented in hardware logic (e.g., an integrated circuit chip, Programmable Gate Array (PGA), Application Specific Integrated Circuit (ASIC), etc.) or a computer readable medium, such as magnetic storage medium (e.g., hard disk drives, floppy disks,, tape, etc.), optical storage (CD-ROMs, optical disks, etc.), volatile and non-volatile memory devices (e.g., EEPROMs, ROMs, PROMs, RAMs, DRAMs, SRAMs, ~~firmware, programmable logic~~, etc.).

Dated: January 29, 2008

By: /Janaki K. Davda/

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